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Circuits and Systems, 2001. ISCAS 2001. The 2001 IEEE  
International Symposium on , Volume: 5 , 6-9 May 2001  
Page(s): 307 -310 vol. 5

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**2 Beyond SPICE with Saber and MAST**

*Mantooth, H.A.; Vlach, M.;*  
Circuits and Systems, 1992. ISCAS '92. Proceedings., 1992 IEEE  
International Symposium on , Volume: 1 , 3-6 May 1992  
Page(s): 77 -80 vol.1

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**3 Modeling and simulation with Saber**

*Vlach, M.;*  
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**4 Computer oriented formulation of equations and analysis of switched-capacitor networks**

*Vlach, J.; Singhal, K.; Vlach, M.;*  
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**5 FDNR switched-capacitor filters insensitive to parasitic capacitances**

*Faruque, S.; Vlach, M.; Vlach, J.; Singhal, K.; Viswanathan, T.;*  
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Page(s): 589 -595

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**2 VHDL-AMS-a hardware description language for analog and mixed-signal applications**

*Christen, E.; Bakalar, K.;*  
Circuits and Systems II: Analog and Digital Signal Processing, IEEE Transactions on , Volume: 46 Issue: 10 , Oct. 1999  
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**3 VHDL 1076.1-analog and mixed-signal extensions to VHDL**

*Christen, E.; Bakalar, K.;*  
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*Christen, E.; Damon, D.;*  
Computer-Aided Control System Design, 1996., Proceedings of the 1996 IEEE International Symposium on , 15-18 Sept. 1996  
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**5 Introduction to VHDL-AMS. 1. Structural and discrete time concepts**

*Damon, D.; Christen, E.;*

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**7 VHDL-A: analog extension to VHDL**

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**8 NETOPT-a program for multiobjective design of linear networks**

*Christen, E.; Vlach, J.;*  
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**9 Poles, zeros, and their sensitivities in switched-capacitor networks**

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